

Future of CMOS and Talent Workshop

Georgia Tech Global Learning Center (GLC)

81 4th St NW, Atlanta, GA 30332

March 26–27, 2026

Thursday, March 26

Opening Session

9:30 AM – 10:00 AM

Coffee and Light Refreshment

10:00 AM – 10:20AM

Welcome and Opening Remarks

Suman Datta

10:20 AM – 11:10 AM

Keynote Address (TBD)

Robert Chau, AMD

11:1- AM – 11:50 AM

Keynote Address (TBD)

Anand Murthy, Lam Research

11:50 AM – 1:00 PM

Lunch (on-site)

Session 1: 3D Memory Technologies

1:00 PM – 1:30 PM

3D DRAM

Kiseok Lee

1:30 PM – 2:00 PM

3D FeRAM

Ekmath Sakar

2:00 PM – 2:30 PM

AOS Reliability

Hyun Jae Lee

2:30 PM – 3:00 PM

Capacitorless 3D eDRAM

Kiseok Lee

3:00 PM – 3:15 PM

Coffee Break

Session 2: Memory Wall and Interconnect Wall

3:15 PM – 3:45 PM

Breaking the Memory Wall for LLM, MoE and Agentic AI

Ming-Yen Lee

3:45 PM – 4:15 PM

Optical Interconnect for LLM, MoE and Agentic AI

Seongwon Yoon

4:15 PM – 5:00 PM

Panel Discussion: *Grand Challenge Research Problems for Industry-Academia Partnership*

Moderator: *Shimeng Yu*

Panelists: *Robert, Anand, Supratik, Suman, etc.*

5:00 PM

Adjourn

6:30 PM

Workshop Dinner (off-site, TBD)

Friday, March 27

9:00 AM – 9:30 AM

National Laboratory's Microelectronics R&D Strategy
Supratik Guha, University of Chicago

Session 3: AI-Assisted Semiconductor Engineering

9:30 AM – 10:00 AM

Digital Twin for Semiconductor Manufacturing
Asif Khan

10:00 AM – 10:30 AM

Agentic Flow for Power Delivery
Visvesh Sathe

10:30 AM – 11:00 AM

Thermal Modeling- Atoms to Systems
Satish Kumar

11:00 AM – 12:15 PM

Marcus Cleanroom Tour

12:15 PM – 1:00 PM

Lunch

1:00 PM

Workshop Adjourns
